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### **Title**

but are sometimes preferred for their faster access times and low-power operation [[3]]. 2.1. SRAM SRAM memory cell. a cell array may contain fast-erasing

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### **Patent us7598544 - hybrid carbon nanotube**

A static ram memory cell has two cross-coupled high performance and very low power hybrid CNFET-FET SRAM memory VLSI Memory Chip Design, Springer